

Parallel data processing for PCB testing

D. Calugari^{1,2}, V. Ababii¹, V. Sudacevski¹, R. Melnic¹, D. Bordian¹ and A. Dubovoi^{1,2}

¹CSSED, TUM, Chisinau, Republic of Moldova

²ICG Engineering, Republic of Moldova
e-mail: victor.ababii@calc.utm.md

PCBs (Printed Circuit Board) inspection and testing requires an analysis of some performance criteria for the data acquisition and processing system. These criteria are determined by the physical properties of the processes, which determine the concurrent propagation of the signals in the PCBs [1] and requires their acquisition and parallel processing [2]. In this case it is necessary to apply the methods and models of concurrent processing of multidimensional digital signals [3].

The major issue in the development of PCB testing systems is the spatial and time synchronization of data acquisition and processing [1, 2]. This problem can be solved by developing new discretization and signal acquisition methods and new mathematical models for their processing.

Let's consider the PCB board with a set of input signals \mathbf{U}^{In} and a set of output signals \mathbf{U}^{Out} , where: $\mathbf{U}^{\text{In}} = u_i^{\text{In}}, i = \overline{1, N}$ with N test input points; and $\mathbf{U}^{\text{Out}} = u_j^{\text{Out}}, j = \overline{1, K}$ with K measuring test points. The mathematical model for measured test signals calculation according to the input test signals \mathbf{U}^{In} and the electrical parameters \mathbf{Z}_j of the PCB is described by the following formula:

$$u_j^{\text{Out}} = g_j(\mathbf{U}^{\text{In}}, \mathbf{Z}_j).$$

The functional scheme for the PCB testing system consists of a test signal generator \mathbf{U}^{In} (designed on an FPGA circuit), a device for the injection of \mathbf{U}^{In} signals on the PCB and for obtaining the measured signals \mathbf{U}^{Out} from the PCB. The identification of the measured signals shape \mathbf{U}^{Out}

is done by their differentiation $\frac{du_j^{\text{Out}}}{dt}$ and further integration $\int_0^T (\dot{u}_j^{\text{Out}}) dt$. The differentiation

function is performed on a variety of electronic differentiation and fuzzy circuits, the result of which is stored and transmitted to a computer. Parameter testing of the PCB takes place on the PC as a result of the analysis of the influence of the input signals \mathbf{U}^{In} on the output signals \mathbf{U}^{Out} :

$$\frac{\partial u_j^{\text{Out}}}{\partial u_i^{\text{In}}}, i = \overline{1, N}, j = \overline{1, K}$$

and the mutual inflection of the output signals \mathbf{U}^{Out} :

$$\frac{\partial u_j^{\text{Out}}}{\partial u_l^{\text{Out}}}, l = \overline{1, K}, j = \overline{1, K}.$$

Functional modeling and interaction of parallel data processing system components for PCB testing was performed on the basis of UML diagrams.

Bibliography

- [1] M. Serban, Y. Vagapov, Z. Chen, R. Holme, and S. Lupin, "Universal platform for PCB functional testing", in *Proc. of Int. Conf. on Actual Problems of Electron Devices Engineering (APEDE-2014)*, 25-26 September 2014, Saratov, Russia, vol. 2, pp. 402-409. DOI: <http://dx.doi.org/10.1109/APEDE.2014.6958285>. Available from: https://www.researchgate.net/publication/288484304_Universal_platform_for_PCB_functional_testing.

- [2] D. Calugari, V. Sudacevski, V. Ababii, D. Bordian. "System for digital processing of multidimensional signals", *Proceedings of the 9th International Conference on Microelectronics and Computer Science & The 6th Conference of Physicists of Moldova*, Chisinau, Moldova, October 19-21, 2017. pp. 336-339, ISBN: 978-9975-4264-8-0.
- [3] D. Dudgeon, and R. Mersereau. *Multidimensional Digital Signal Processing*, Prentice-Hall, First Edition, 400 p. 1983, ISBN: 978-0136049593.

Parallel algorithm to determine the Nash solutions in bimatrix games

Cataranciu Emil

Moldova State University, Chisinau, Republic of Moldova
e-mail: ecataranciu@gmail.com

Given a bi-matrix game $\Gamma = \langle I, J, A, B \rangle$ where I – the row index set of the matrix, J – the column index set of the matrix, $\|a_{ij}\|_{\substack{j \in J \\ i \in I}}$ and $\|b_{ij}\|_{\substack{j \in J \\ i \in I}}$ are the players' payoff matrices. We construct the following parallel algorithm to determine equilibrium profiles.

- 1) Data parallelization. The MPI process with rank 0 initializes matrices A and B , then distributes, based on the 2D cyclic algorithm, the sub-matrices obtained by each MPI process from the virtual 2-dimensional topology communicator. Thus, each MPI process will only work with sub-matrices obtained as a result of distribution.
- 2) Each process from the virtual 2-dimensional topology communicator will eliminate, in parallel, from the sub-matrices of the matrices A and B that it possesses based on the step 1), those rows that are dominated in matrix A and those columns that are dominated in matrix B .
- 3) Determination of the equilibrium strategies for the matrix (A', B') , $A' = \|a'_{ij}\|_{\substack{i \in I' \\ j \in J'}}$ and $B' = \|b'_{ij}\|_{\substack{i \in I' \\ j \in J'}}$ obtained in step 2). It's clear that $|I'| \leq |I|$ and $|J'| \leq |J|$. For this using the reduction operations, each process will determine $i^*(j) = \arg \max_{i \in I'} a'_{ij}$ for any $j \in J'$ and $j^*(i) = \arg \max_{j \in J'} b'_{ij}$ for any $i \in I'$.
- 4) We select those index pairs that are simultaneously selected in both matrix A' and matrix B' . In other words, it is determined $\begin{cases} i^* \equiv i^*(j^*) \\ j^* \equiv j^*(i^*) \end{cases}$ which is actually the intersection of the graphs of the following point to set applications $i^*(\cdot)$ and $j^*(\cdot)$.
- 5) The equilibrium strategies for the game with the initial matrices A and B are built.